

Electronic Acknowledgement Receipt

EFS ID:	2149053
Application Number:	10757788
International Application Number:	
Confirmation Number:	5462
Title of Invention:	Method and apparatus for the automatic correction of faulty wires in a logic simulation hardware emulator / accelerator
First Named Inventor/Applicant Name:	Thomas Michael Gooding
Customer Number:	30206
Filer:	James R. Nock/Deb Peterson
Filer Authorized By:	James R. Nock
Attorney Docket Number:	ROC920030335US1
Receipt Date:	31-AUG-2007
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	yes
Payment was successfully received in RAM	\$790
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	ROC920030335US1_RequestforContinuedExam_08-31-07.pdf	742242 363136e41d28349b02263d26011216b 3b1608398	no	3
Warnings:					
Information:					
2	Amendment Submitted/Entered with Filing of CPA/RCE	ROC920030335US1_PreliminaryAmendment_08-31-07.pdf	608528 b01a0d6d21afde23a72f9069db3edfe23 d8e0af2	no	16
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8235 6b3a88ff3ae669774fed87e0cff332af7d d4f43e	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				1359005	
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